

FORM PTO/SB/08A/B (10-01)  
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Attorney Docket Number	41962/DBP/S632
Application Number	09/800,966
Filing Date	March 6, 2001
Applicant(s)	Richard Lauder, et al.
Group Art Unit	2633
Examiner Name	Reza Sedighian

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
MRS		5,371,766	12-06-1994	Gersbach et al.
MRS		6,288,811	09-11-2001	Jiang et al.
MRS		6,335,992 B1	01-01-2002	Bala et al.
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MRS		6,535,313	03-18-2003	Fatehi et al.

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EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)
MRS		EP 1 091 614 A2	04-11-2001	Alan F. Graves, et al.	
MRS		WO 99/40700	08-12-1999	Joseph E. Berthold, et al.	
MRS		WO 00/55995	09-21-2000	Egnell	
MRS		WO 00/67408	11-09-2000	Ronald Mackey	

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MRS		<a href="http://www.amcc.com/Compinfo/PressReleases/S3086">http://www.amcc.com/Compinfo/PressReleases/S3086</a> , "AMCC Provides Unparalleled Combination of Jitter Performance, Input Sensitivity and Low Power for Metro DWDM Applications with Continuous Rate Clock and Data Recovery Device", July 20, 2004, 2 pages
MRS		<a href="http://www.amcc.com/Compinfo/PressReleases/S3062pr">http://www.amcc.com/Compinfo/PressReleases/S3062pr</a> , "AMCC Adds Forward Error Correction to its DWDM Multi-Rate Solutions", July 20, 2004, 2 pages

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MRS		Gaudino, et al., "MOSAIC: A Multiwavelength Optical Subcarrier Multiplexed Controlled Network", IEEE Journal on Selected Areas in Communications, September 1998, Vol. 16, No. 7, pp 1270-1285
MRS		Rossi, et al., "Optical Network Channel Protection Switching Demonstration using a Bi-Directional Reconfigurable Multichannel Add/Drop Multiplexer", 1999 Technical Digest, 2000 Optical Society of America, 07/21/1999, Vol. 32, pp 100-102, XP008008948
MRS		Scheytt, J, "A 0.155-, 0.622-, and 2.488-Gb/s Automatic Bit-Rate Selecting Clock and Data Recovery IC for Bit-Rate Transparent SDH Systems", IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp 1935-1943

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